Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	2	11 and (interrupt\$3 event tim\$3 synchron\$7 asynchron\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/30 09:59
L2	2	11 and (interrupt\$3 event tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/30 09:59
El	2	."6496847".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/30 09:52
S61	218	((switch\$3 chang\$3) with (thread\$3 task\$3 process) with (context state) with (cpu processor)) same ((stor\$3 sav\$3) with (context state) with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 17:32
S22	666	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3) with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 17:30
S60	60	S59 not S57	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:50
S59	67	((switch\$3 chang\$3) with (context state) with (cpu processor)) and ((stor\$3 sav\$3) with (context state) with (shadow\$3 near3 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:50
\$56	7	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3) with shadow\$3 with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:49
S58	1	:S57 not:S56	US-PGPUB; USPAT; EPO; JPO; DERWENT; ; IBM_TDB	OR	ON	2005/03/28 16:44

S57	-	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3)) same (shadow\$3 near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:43
S55	4	inconsequential near3 register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM: TDB	OR	ON	2005/03/28 16:26